

Fig. 1A

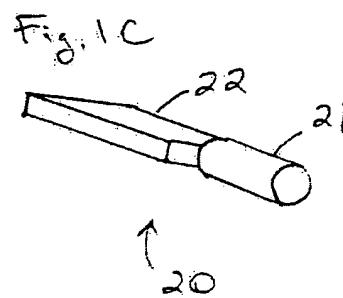
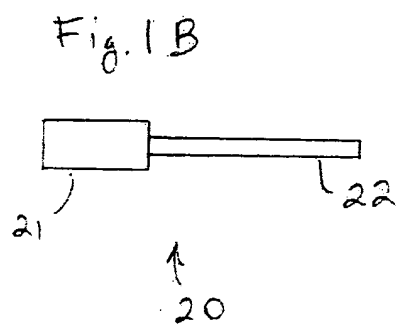


Fig. 2A

Equivalent Circuit

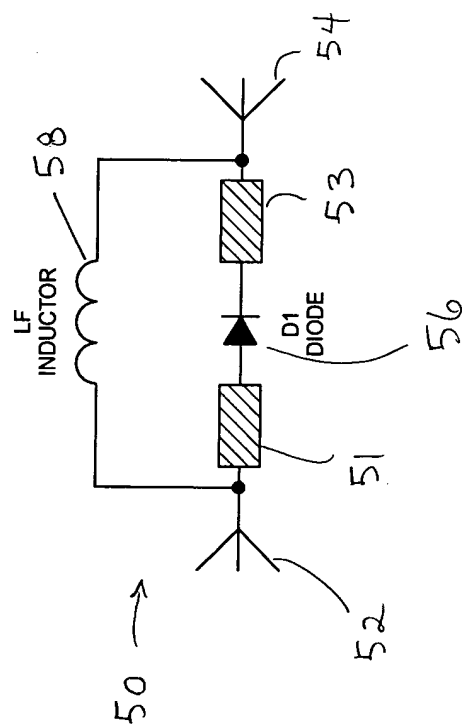


Fig. 2B

Actual Circuit

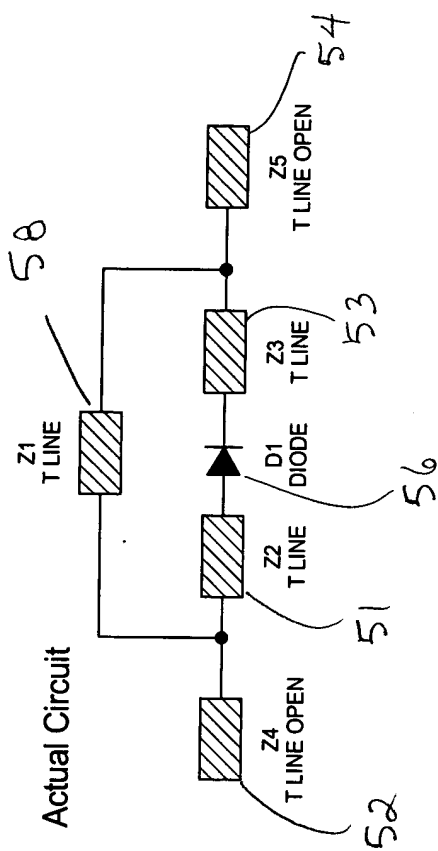
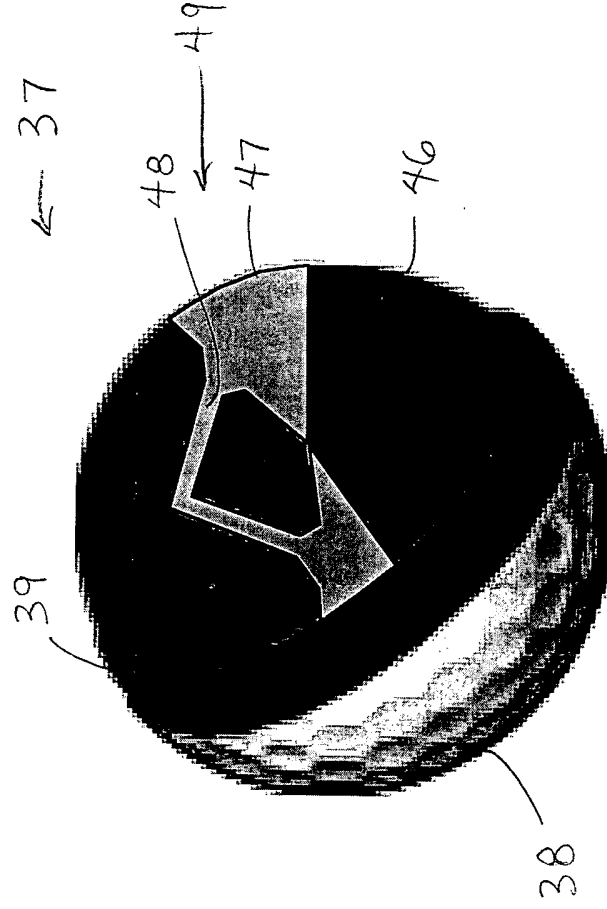


Fig. 3A



ANTENNA ON CORE
(not to scale)
Shown on a 3-piece ball

Fig. 3B

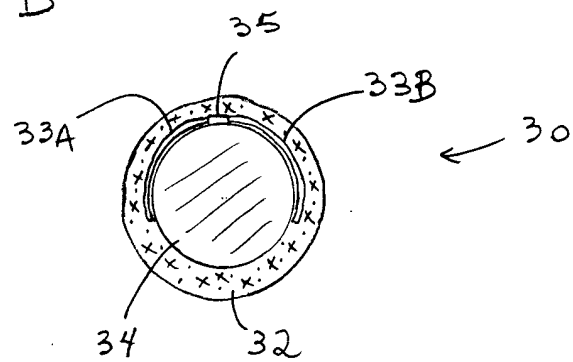


Fig. 3C

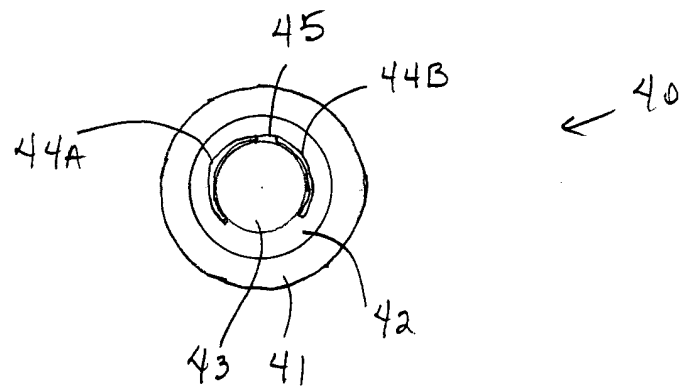


Fig. 4A

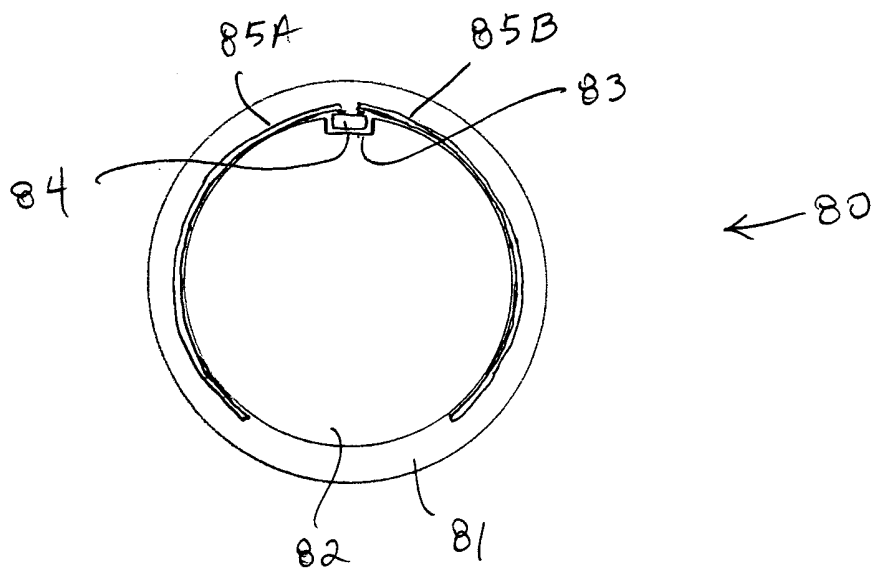


Fig. 4B

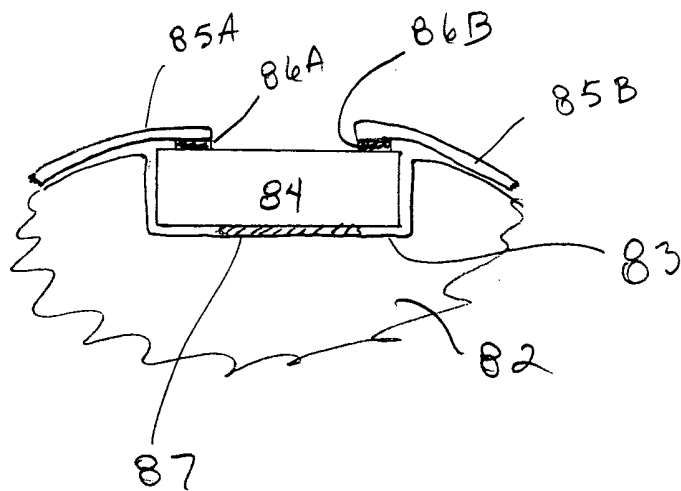


Fig. 4C

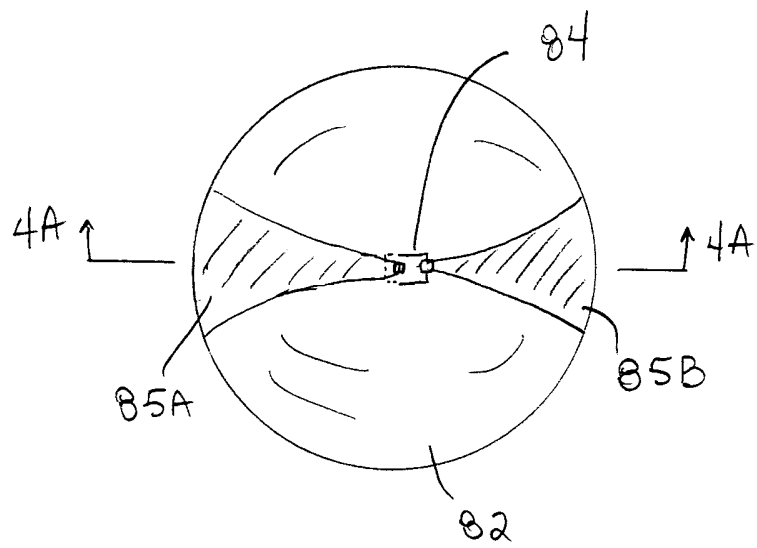


Fig. 5A

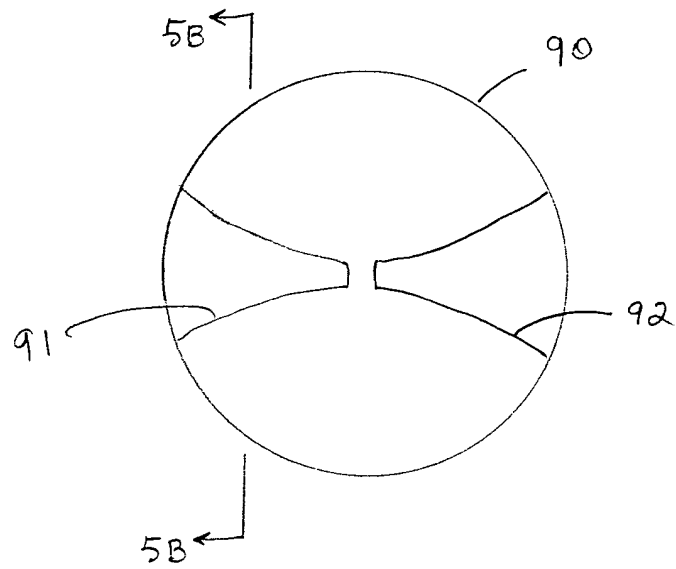


Fig. 5B

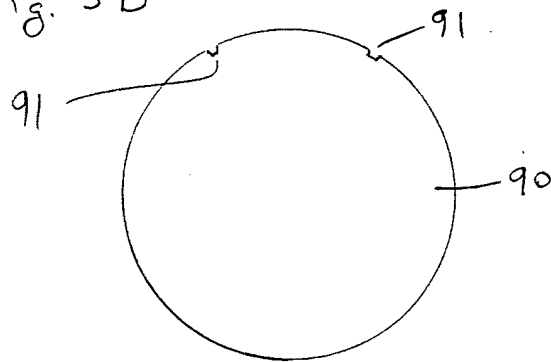


Fig. 6A

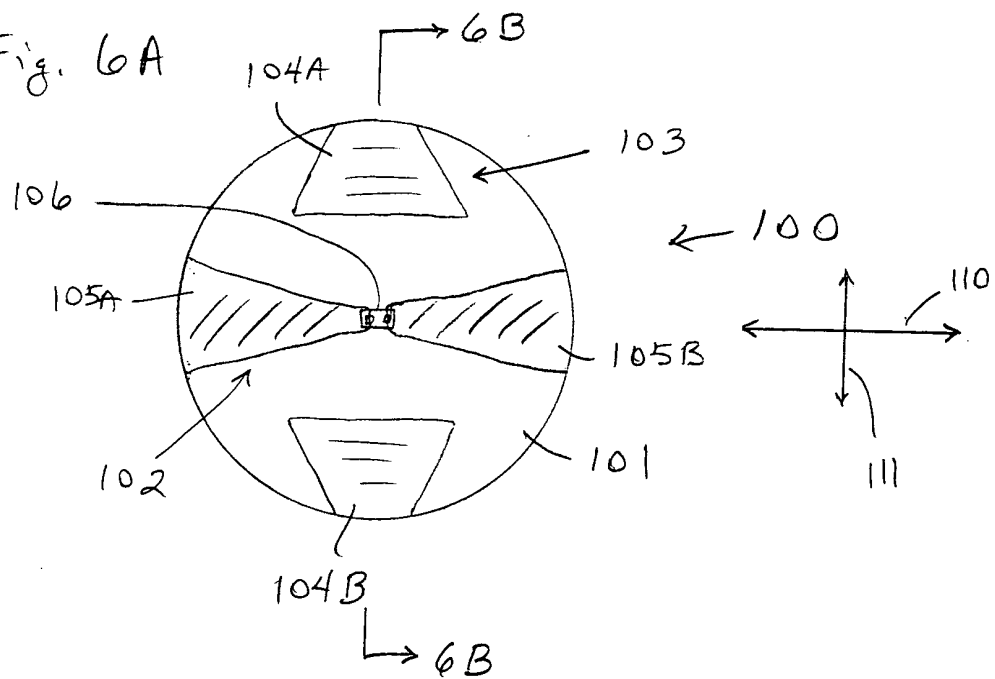


Fig. 6B

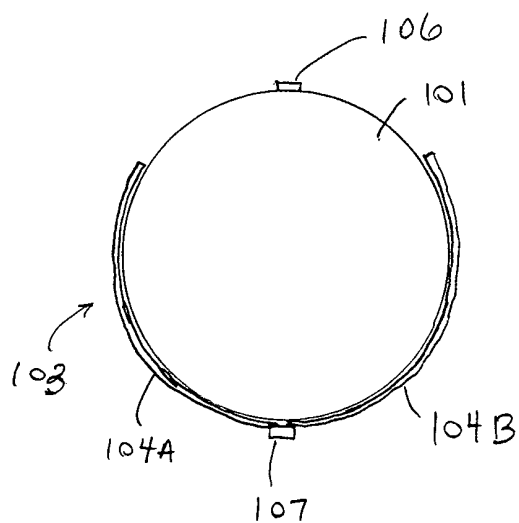
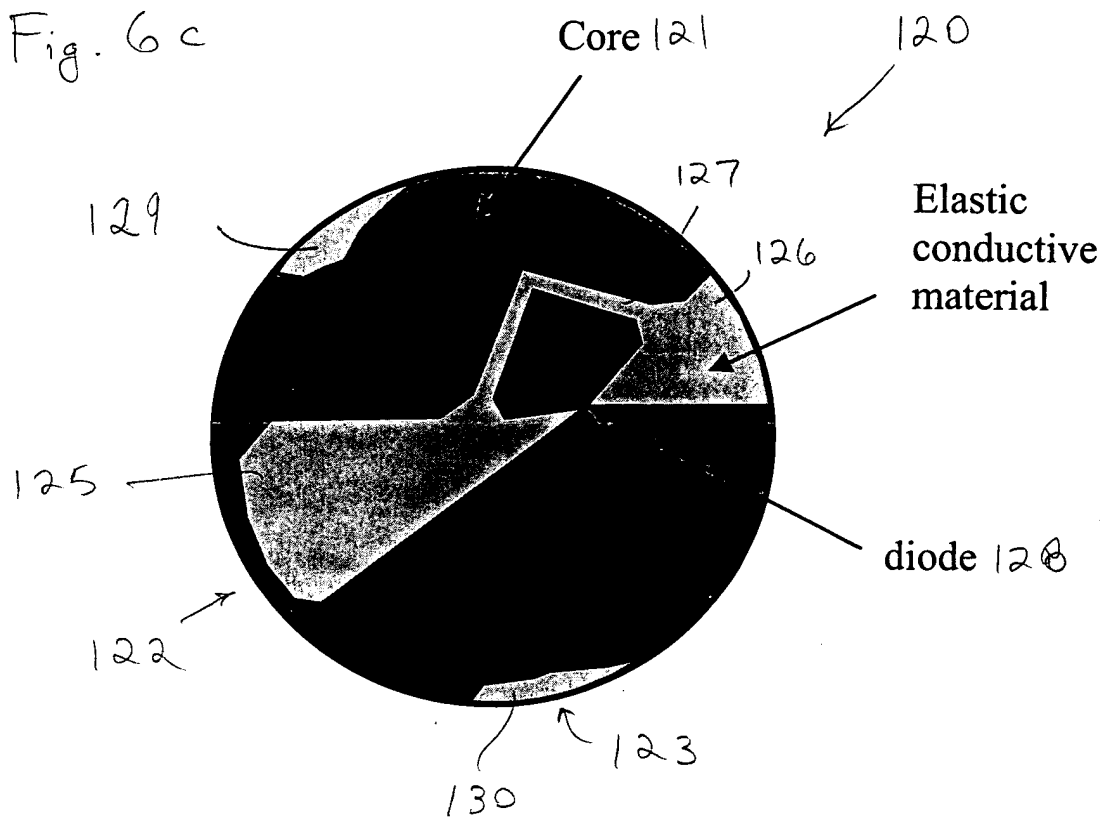


Fig. 6c



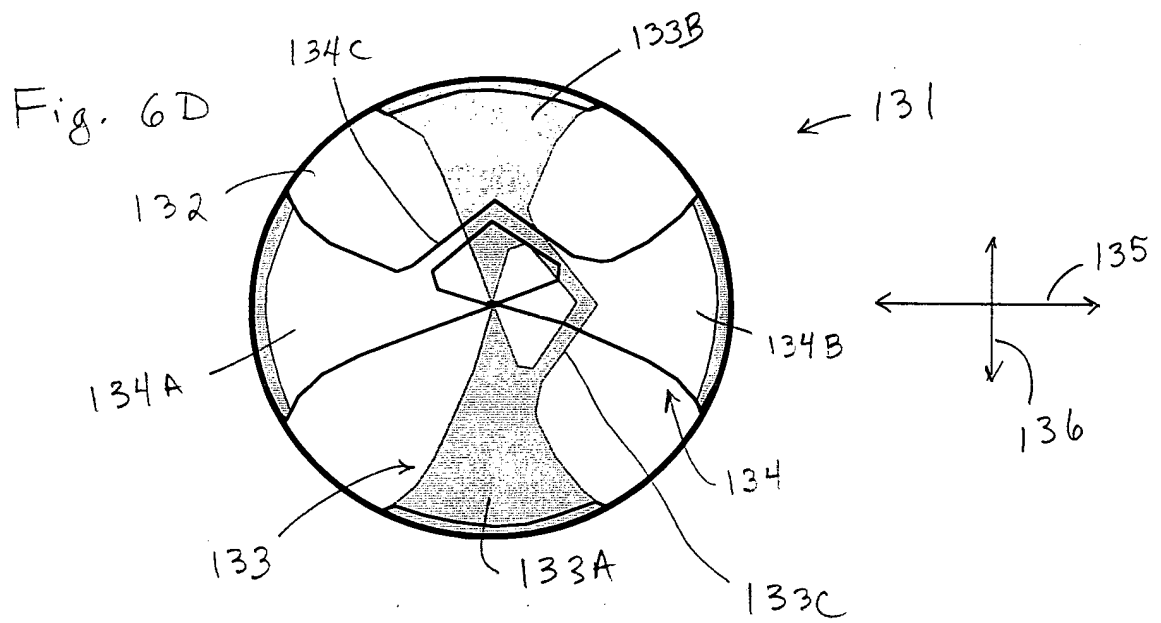
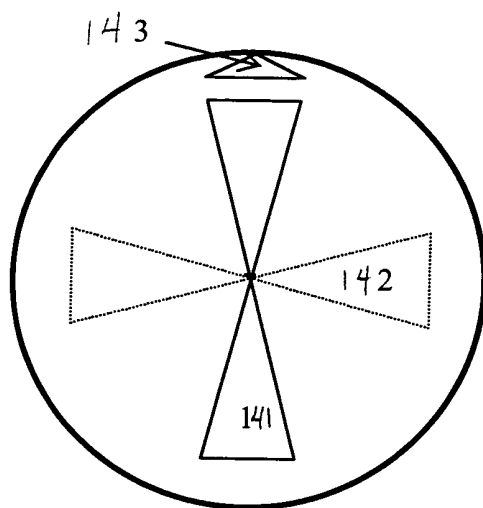
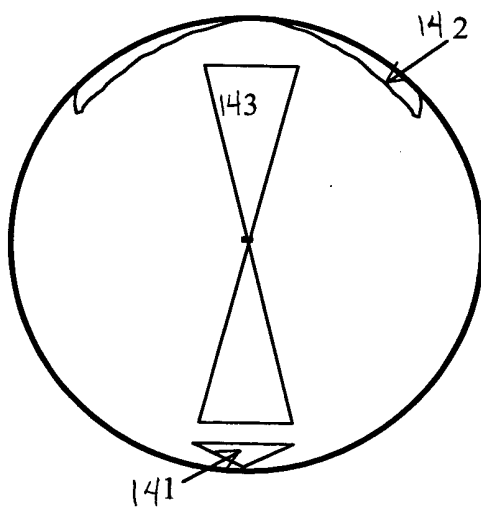


Fig. 6E



Side View

Fig. 6F



Top View

Fig. 7A

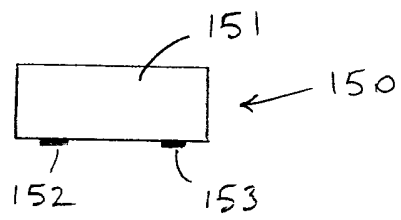


Fig. 7B

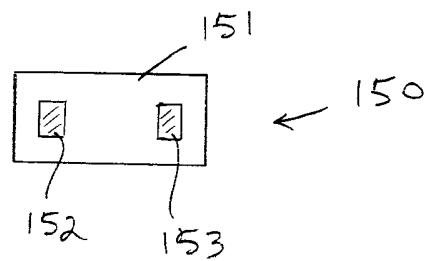
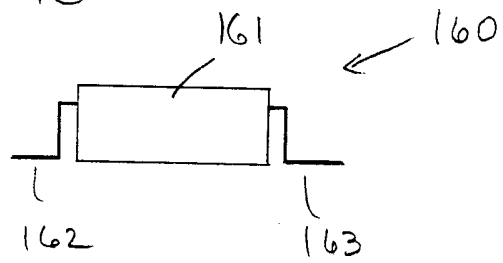
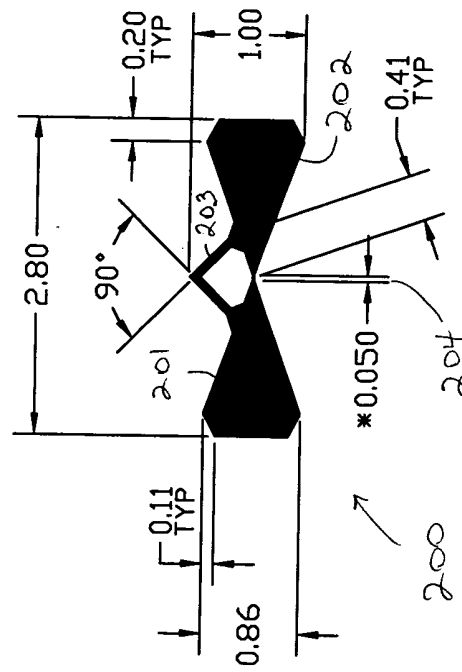


Fig. 7C

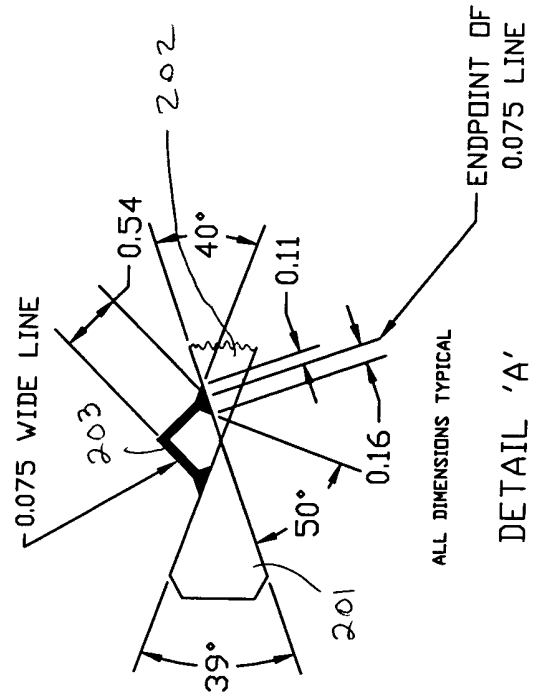


4000



* 0.050 GAP FOR 0503 DIODE PACKAGE,
* 0.075 GAP FOR SDD323 DIODE PACKAGE

11.50



ALL DIMENSIONS TYPICAL

DETAIL 'A'

ENDPOINT OF
0.075 LINE

Fig. 8C

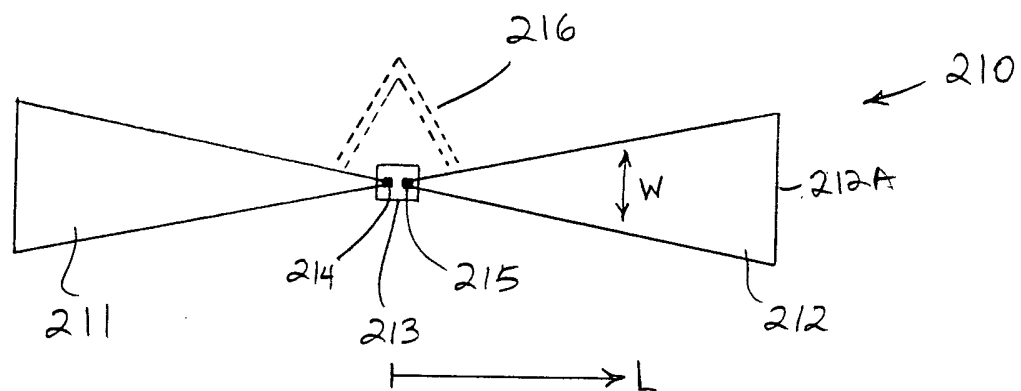
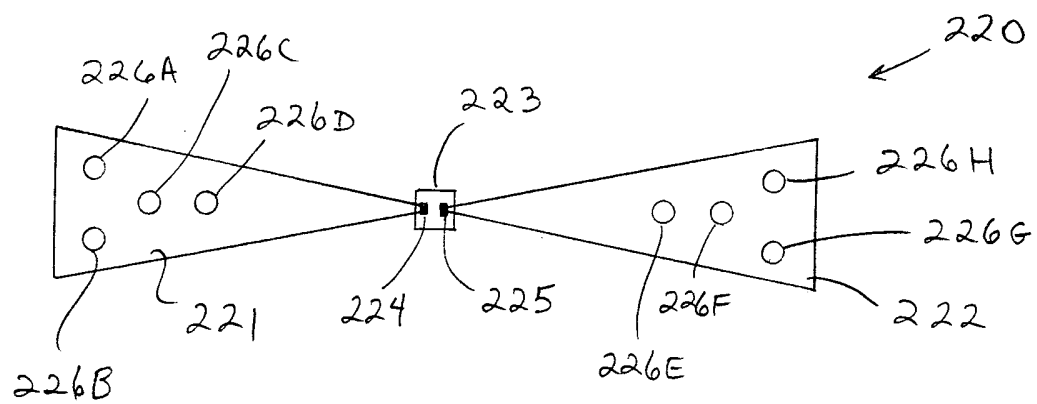


Fig. 8D



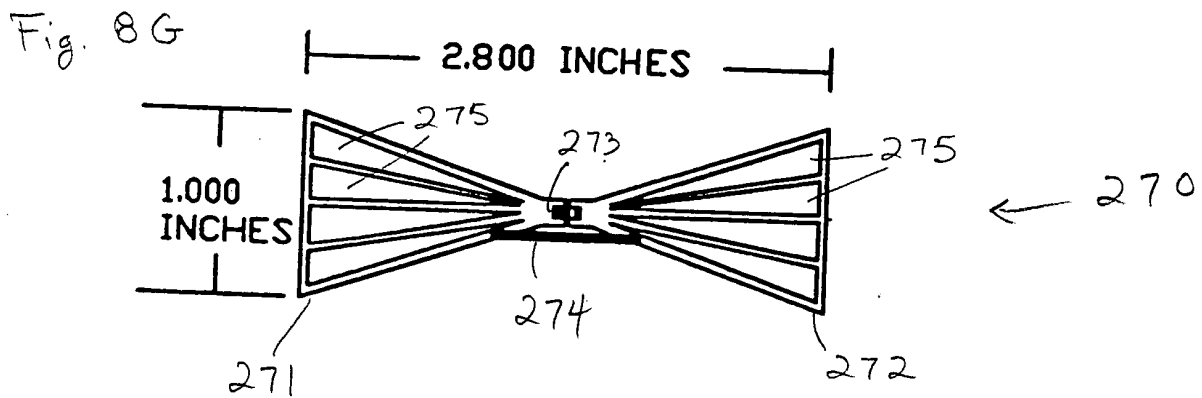
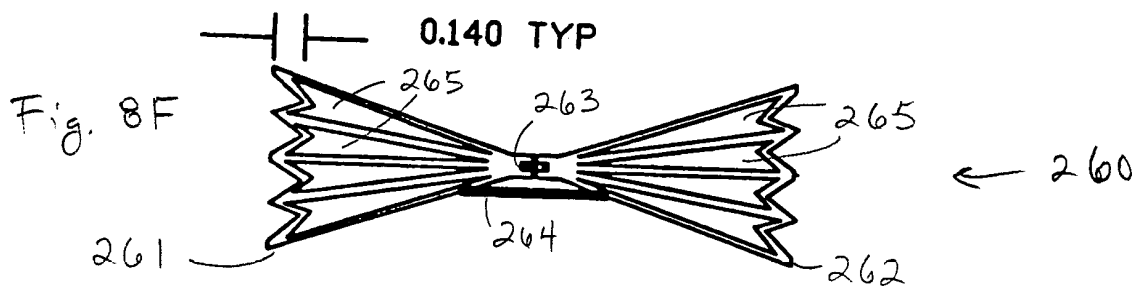
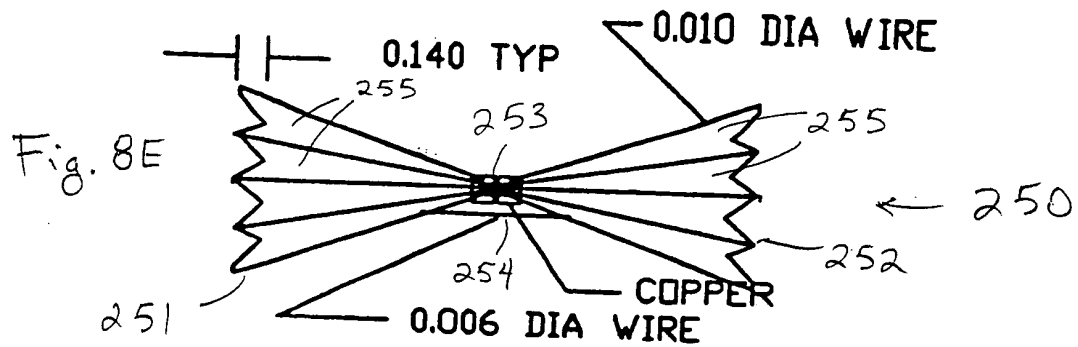


Fig. 9A

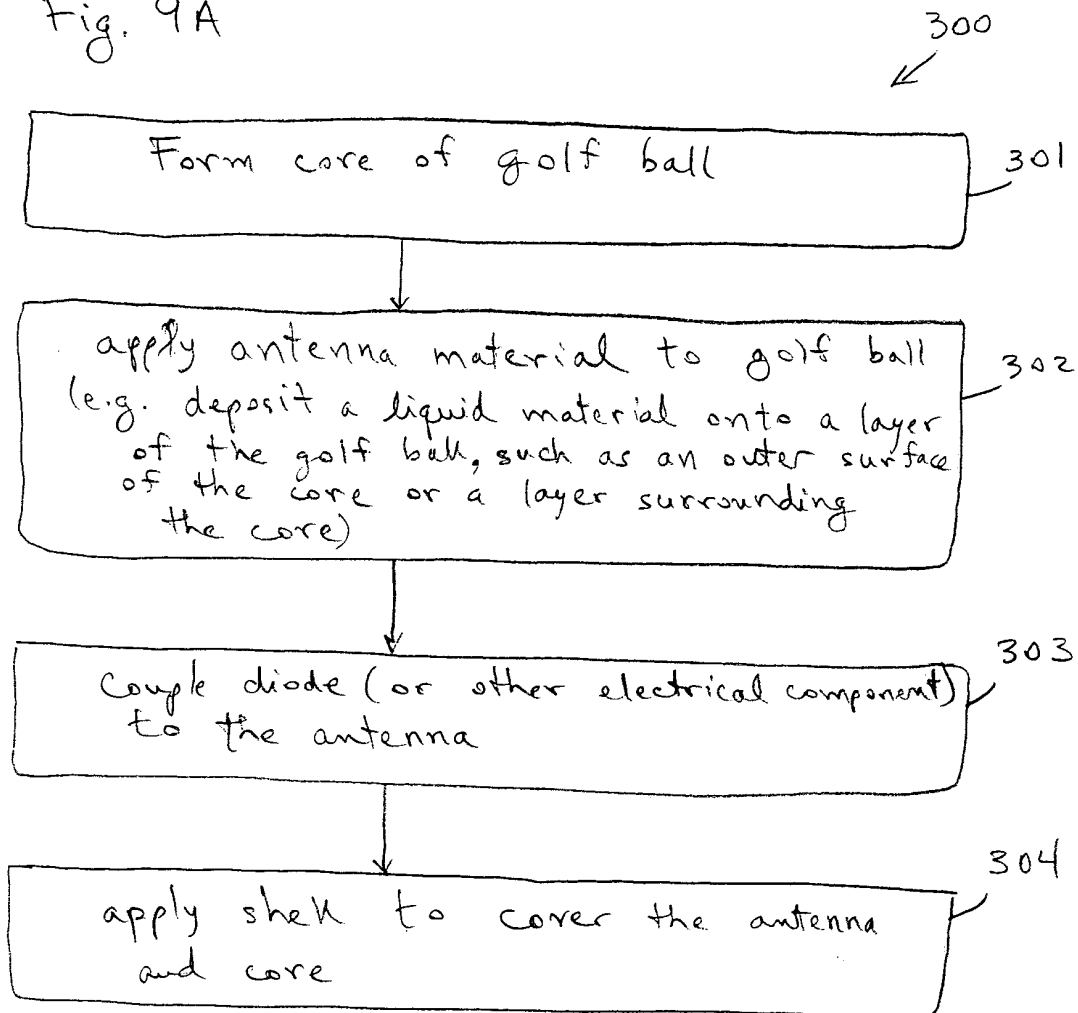


Fig. 9B

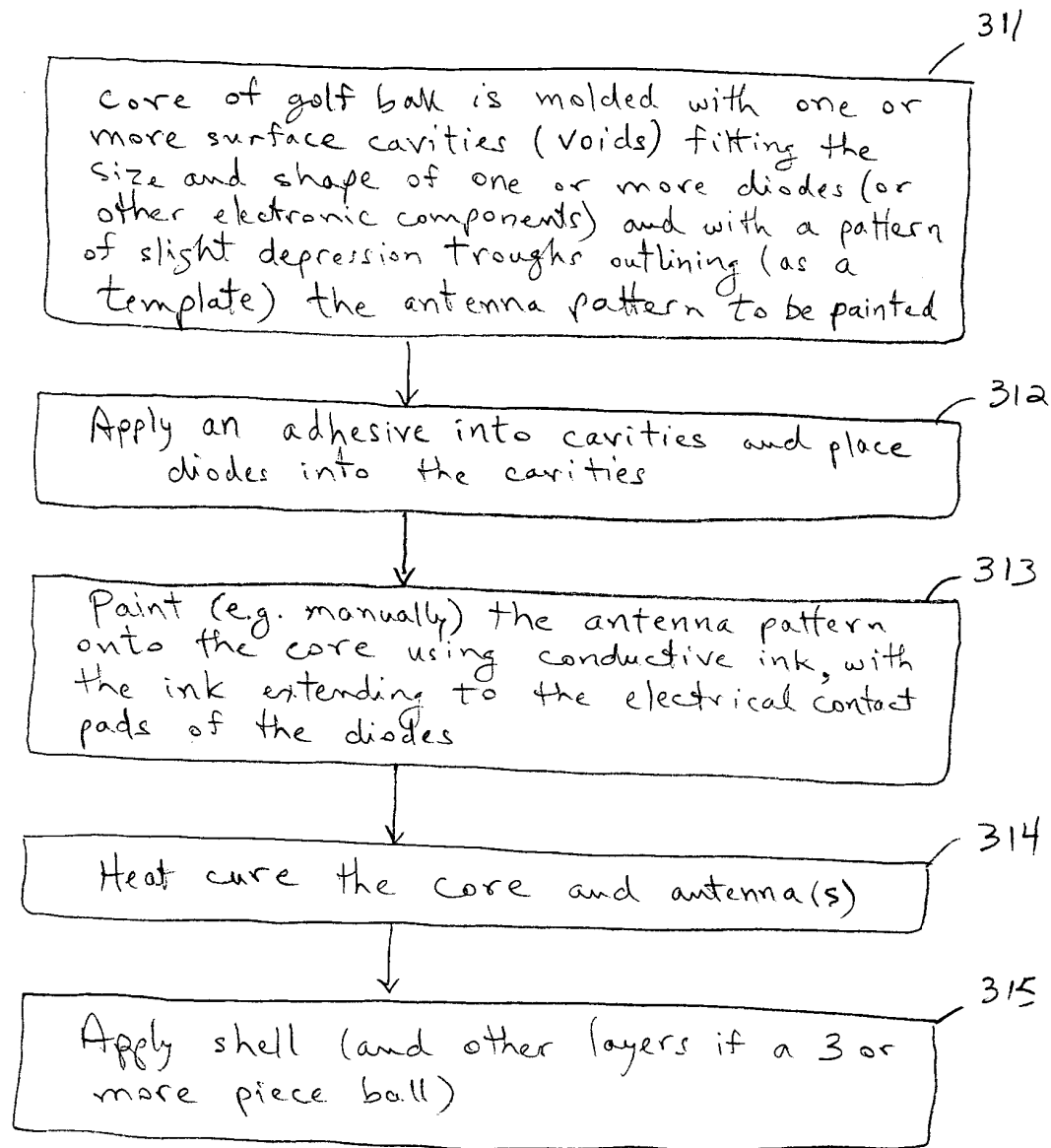


Fig. 9C

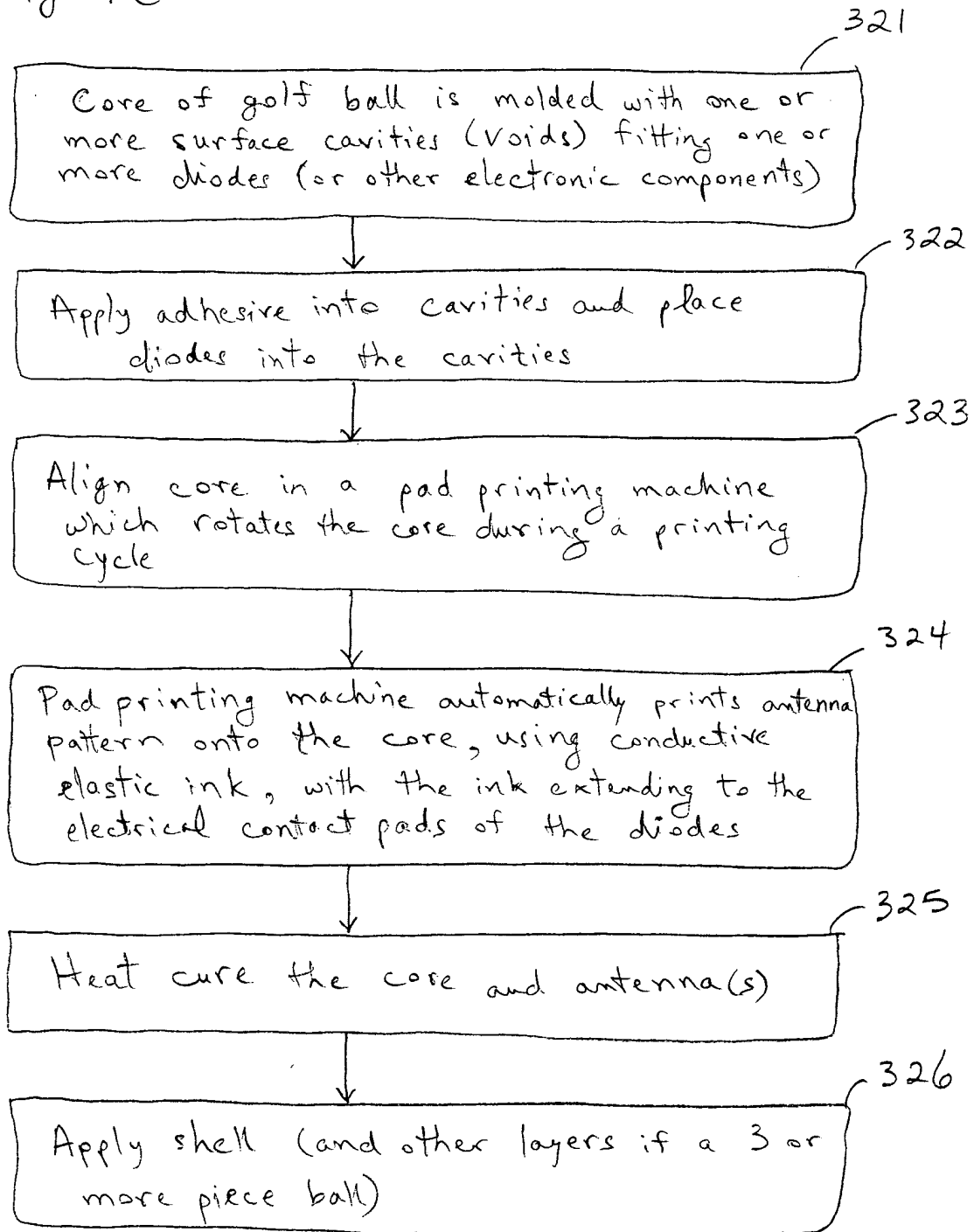


Fig. 9D

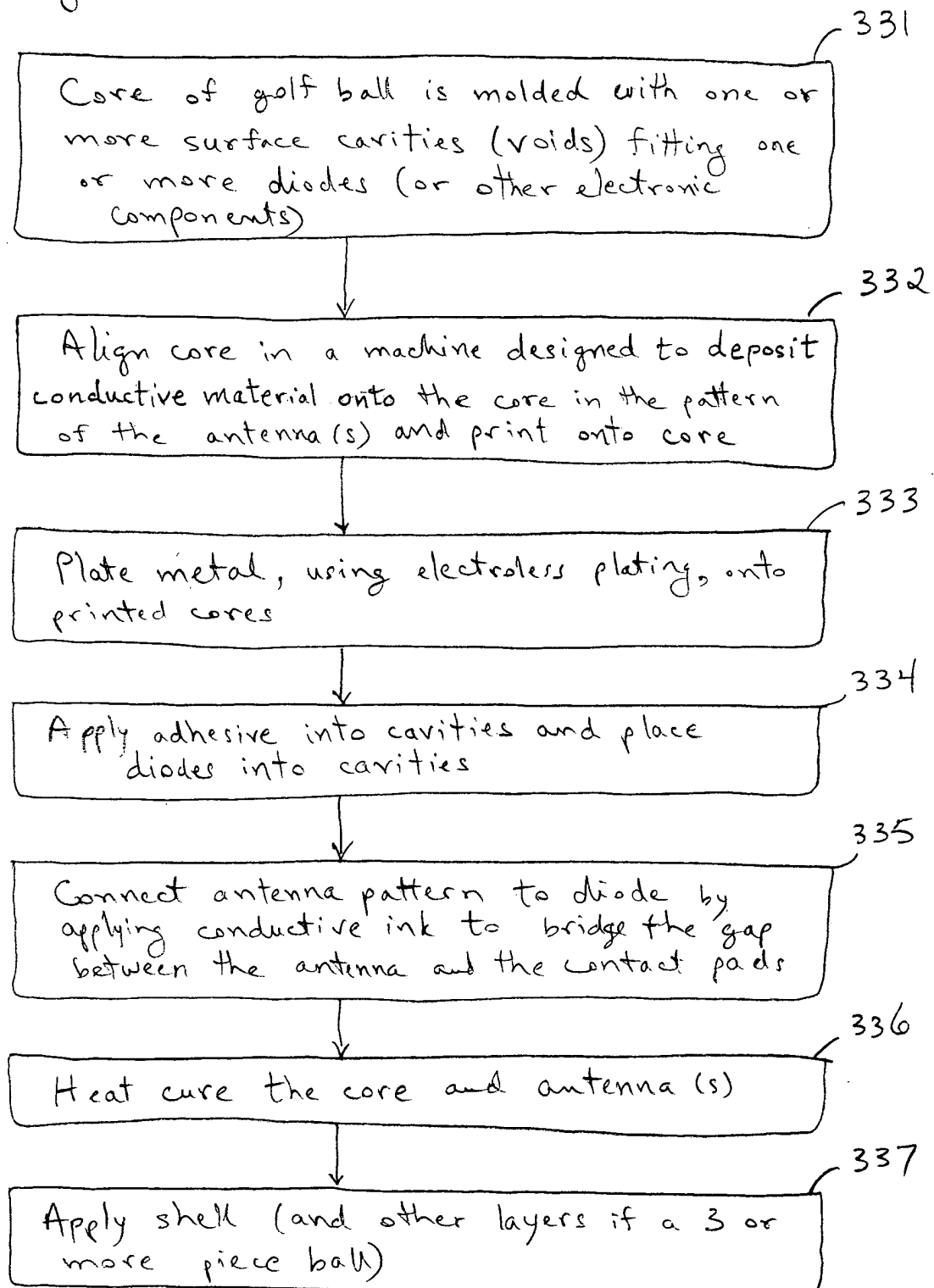


Fig. 9E

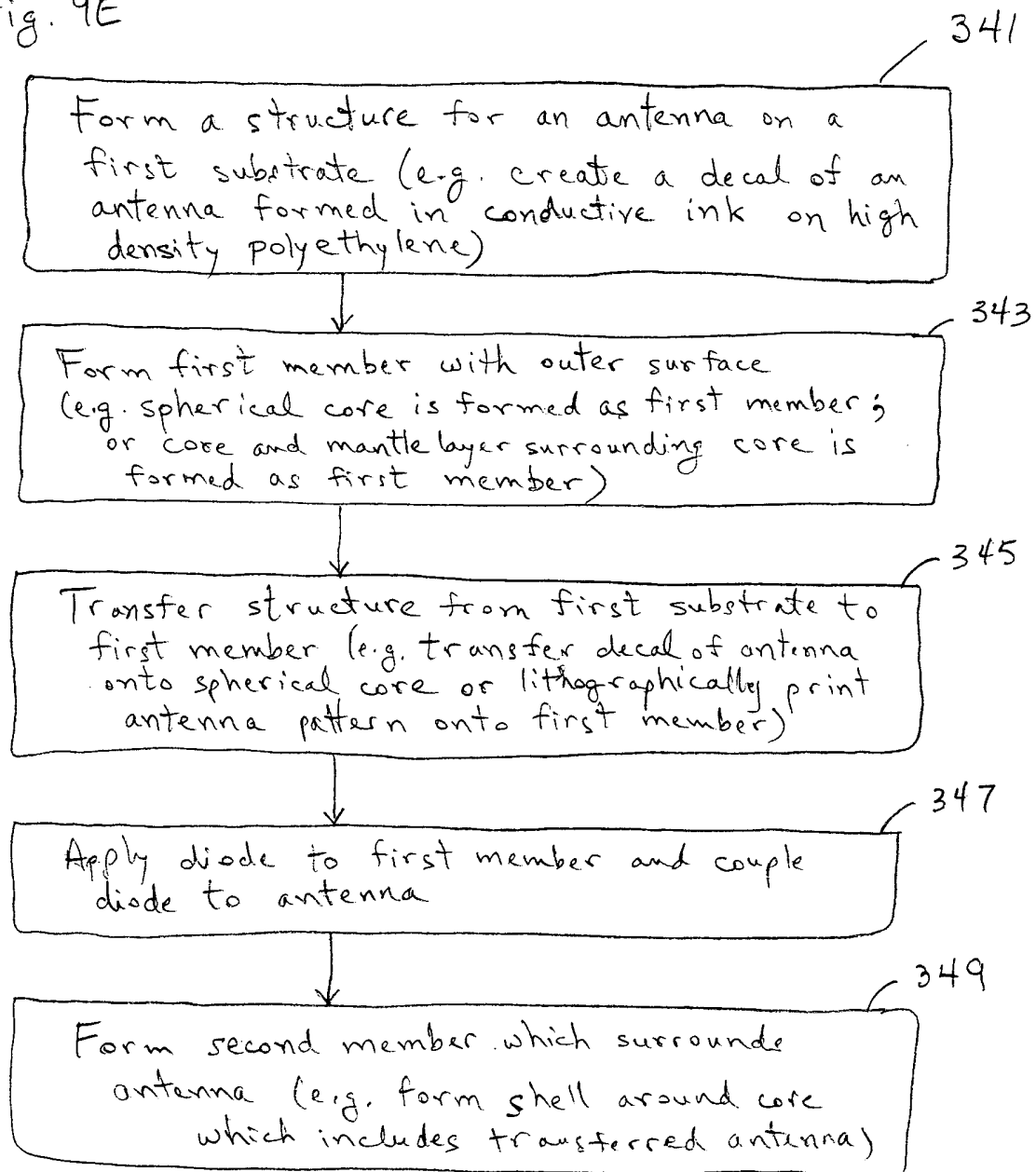


Fig. 10

